- 1. The traveling salesman problem can be defined in two ways:
 - The Traveling Salesman Problem
 - INPUT: A weighted graph G
 - OUTPUT: Which tour $(v_1, v_2, ..., v_n)$ minimizes $\sum_{i=1}^{n-1} (d[v_i, v_i + 1]) + d[v_n, v_1]$
 - The Traveling Salesman Decision Problem
 - INPUT: A weighted graph G and an integer k
 - OUTPUT: Does there exist and TSP tour with cost $\leq k$

Suppose we are given an algorithm that can solve the traveling salesman decision problem in (say) linear time. Give an efficient algorithm to find the actual TSP tour by making a polynomial number of calls to this subroutine.

Solution: We will first find the minimal cost of a TSP tour, and then find the minimal TSP tour using the minimal cost. We let TSPD(G, k) denote the result of running the given algorithm for traveling salesman decision problem on the input (G, k).

Finding the minimal cost : Let d_{min} and d_{max} denote the minimal and maximal weight of the edges in *E*, respectively. Since a TSP tour would contain |V| edges, we can infer that the cost of any TSP tour would be lower-bounded by $|V|d_{min}$ and upper-bounded by $|V|d_{max}$. Therefore, we can run binary search on the integers ranging from $|V|d_{min}$ to $|V|d_{max}$ to find the integer value *k* such that TSPD(G, k) is True and TSPD(G, k-1) is False. The integer *k* would be the minimal cost of a TSP tour.

Finding the minimal TSP tour : Let *k* be the minimal cost of a TSP tour in *G*. Let *e* be an arbitrary edge of *G* and let *G'* be a graph obtained by removing *e* from *G*. Suppose *e* was not included in a minimal TSP tour *T* of *G*(note that minimal TSP tour is not necessarily unique). Then TSPD(G', k) would return True, since the minimal TSP tour *T* still remains in *G'*. Now suppose *e* was included in every minimal TSP tour of *G*. Then TSPD(G', k) would return False, since without *e* we cannot formulate a minimal TSP tour with the cost *k*. Therefore, for each $e \in E$, we will remove *e* from *G* and run TSPD(G, k). If TSPD(G, k) returns True, that means there exists a minimal TSP in *G* that does not contain *e*, so we will leave *e* removed. If TSPD(G, k) returns False, that means *e* is definitely a part of any minimal TSP in *G*, so we will add *e* back into *G*. Once we have all unnecessary edges removed, we can run a linear time search algorithm from any node to construct the tour. The following is the pseudocode of the algorithm.

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\frac{\text{TSP}(G(V, E)):}{lower \leftarrow |V| * d_{min}}
upper \leftarrow |V| * d_{max}
k \leftarrow \text{BINSEARCH}(lower, upper)
for e in E:

remove e from G

if TSPD(G, k)=False:

add e to G

s \leftarrow \text{ArBITRARYNODE}(G)

tour \leftarrow \text{DFS}(s)

return tour
```

BINSEARCH(lower,upper)):
if upper — lower < 1000
find k via brute force
else
$mid \leftarrow \frac{(lower+upper)}{2}$
if TSPD(G, mid) is true
$k \leftarrow \text{BinSearch(lower,mid)}$
else
$k \leftarrow BinSearch(mid+i,upper)$
return <i>k</i>

The complexity of the binary search is dependent on the difference between the upper and lower bound of the total cost. That is, if we let $c = d_{max} - d_{min}$, the complexity of the binary search is $O(\log(c|V|))$. However, note that $c = O(2^n)$ where *n* is the length(number of bits) of the input, since with *n* bits you can only represent integers upto 2^n . Therefore, the complexity of the binary search is $O(\log(c|V|)) = O(\log c) + O(\log |V|) = O(n)$, which is linear in the input size. The rest of the algorithm loops over every edge, and TSPD is assumed to be linear (O(V + E)), so the runtime of the tour construction is O(E(E + V)). Therefore, the total runtime would be O(n + E(E + V)) which is polynomial in input size. For grading, the reasoning for the complexity of binary search is not required; providing O(E(E + V))and reasoning about the tour construction is sufficient for full marks.

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- 2. For any integer *k*, the problem *k*SAT is defined as follows:
 - INPUT: A boolean formula Φ in conjunctive normal form, with exactly k distinct literals in each clause.
 - OUTPUT: TRUE if Φ has a satisfying assignment, and FALSE otherwise.
 - (a) Describe and analyze a polynomial-time reduction from **2**SAT to **3**SAT, and prove your reduction is correct.

Solution: One such reduction (of infinitely many possible ones) is as follows. Let

$$\Phi = \bigwedge_{i=1}^{n} \ell_{i,1} \vee \ell_{i,2}$$

be the instance to **2**SAT; in the above description of Φ , $\ell_{i,1}$ and $\ell_{i,2}$ are *literals* for all $1 \le i \le n$, not variables. Construct **3**CNF formula

$$\Phi' = \bigwedge_{i=1}^{n} (\ell_{i,1} \lor \ell_{i,2} \lor x) \land (\ell_{i,1} \lor \ell_{i,2} \lor \overline{x});$$

here, *x* is a variable *not* in Φ . Input Φ' into the black box algorithm \mathscr{A} for **3**SAT, and feed the output of \mathscr{A} as the output of the constructed algorithm for **2**SAT. Φ' has exactly twice the number of clauses as Φ and there are at most 2n variables. Thus, Φ' can be constructed by brute force in *time O(n)* by a scanning through once Φ . The reduction is linear-time and thus polynomial-time.

We now prove the correctness of this reduction by proving the following claim: Φ has a satisfying assignment $\iff \Phi'$ has a satisfying assignment.

- ⇒ Suppose there is an assignment A of the variables in Φ that makes Φ evaluate to TRUE. Fix $1 \le i \le n$. By the definition of \land , we have that $\ell_{i,1} \lor \ell_{i,2}$ evaluates to TRUE under A. By the definition of \lor , this gives either $\ell_{i,1} =$ TRUE or $\ell_{i,2} =$ TRUE under A. Define the assignment A' as one that coincides with A for variables in Φ and assigns *any* truth value to *x*. By the definition of \lor , both $\ell_{i,1} \lor \ell_{i,2} \lor x$ and $\ell_{i,1} \lor \ell_{i,2} \lor \overline{x}$ evaluate to TRUE under A'. Since this analysis holds for all $1 \le i \le n$, by the definition of \land , we have that $\bigwedge_{i=1}^{n} (\ell_{i,1} \lor \ell_{i,2} \lor x) \land (\ell_{i,1} \lor \ell_{i,2} \lor \overline{x}) = \Phi'$, which implies Φ' has a satisfying assignment.
- ⇐ Suppose there is an assignment A' of the variables in Φ' that makes Φ' evaluate to TRUE. Fix $1 \le i \le n$. By the definition of \land , $(\ell_{i,1} \lor \ell_{i,2} \lor x) \land (\ell_{i,1} \lor \ell_{i,2} \lor \overline{x})$ evaluates to TRUE under A'. By the definition of \land again, $\ell_{i,1} \lor \ell_{i,2} \lor x$ and $\ell_{i,1} \lor \ell_{i,2} \lor \overline{x}$ both evaluate to TRUE under A'. It can easily be seen that both x and \overline{x} cannot be TRUE under A'. Assume that x is TRUE under A' without loss of generality. Then \overline{x} evaluates to FALSE, which implies that either $\ell_{i,1}$ or $\ell_{i,2}$ must evaluate to TRUE. We prove this by contradiction. Suppose both $\ell_{i,1}$ and $\ell_{i,2}$ evaluate to FALSE. Then $\ell_{i,1} \lor \ell_{i,2} \lor \overline{x}$ evaluates to FALSE, a contradiction. By the definition of \lor , then $\ell_{i,1} \lor \ell_{i,2}$ evaluates to TRUE under A' (and the restriction of the assignment A of A' to variables in Φ). Since this analysis holds for all $1 \le i \le n$, by the definition of \land , we have that

 $\bigwedge_{i=1}^{n} \ell_{i,1} \lor \ell_{i,2}$ evaluates to True under A. But $\bigwedge_{i=1}^{n} \ell_{i,1} \lor \ell_{i,2} = \Phi$, which implies Φ has a satisfying assignment.

(b) Describe and analyze a polynomial-time algorithm for **2**SAT. [Hint: This problem is strongly connected to topics earlier in the semester.]

Solution: Let

$$\Phi = \bigwedge_{i=1}^{n} \ell_{i,1} \lor \ell_{i,2}$$

be the instance to 2SAT; in the above description of Φ , $\ell_{i,1}$ and $\ell_{i,2}$ are *literals* for all $1 \le i \le n$, not variables. Construct a *directed* graph G = (V, E) as follows:

- *x* is a variable in $\Phi \iff x, \overline{x} \in V$
- $\ell_1 \lor \ell_2$ is a clause for some *literals* ℓ_1 and ℓ_2 in $\Phi \iff \overline{\ell}_1 \to \ell_2, \overline{\ell}_2 \to \ell_1 \in E$

Compute the strong components of *G* using Kosaraju's algorithm and check if, for any variable *x*, *x* and \overline{x} are in the same strong component. If so, return FALSE. Otherwise, return TRUE. Kosaraju's algorithm and checking the above condition combined require time O(V + E) in terms of the graph *G*. Since $V \le 2n$ and $E \le 2n$ where *n* is the number of clauses in Φ , in terms of the original input Φ , this algorithm requires *time* O(n). This verifies that the algorithm is indeed polynomial-time.

(c) Why don't these results imply a polynomial-time algorithm for 3SAT?

Solution: We do not have enough information. It's worth noting that either of the following changes to the prompts of parts (a) and (b) would imply a polynomial-time algorithm for **3**SAT:

- Part (a) asks for polynomial-time reduction from **3**SAT to **2**SAT instead of from **2**SAT to **3**SAT.
- Part (b) asks for a polynomial-time algorithm for **3**SAT instead of **2**SAT.

Also, just because you can use a harder problem (in this case **3**SAT) to solve an easier one (in this case **2**SAT) doesn't mean that is the *only* way to solve **2**SAT (as you can see in part (b)). This is a subtle but very important distinction that is at the core of reductions.

- 3. A disjunctive normal form (DNF) formula is the converse of CNF; i.e., it is an ∨ of a number of clauses where each clause is an ∧ of some terms. E.g: (x ∧ y ∧ z) ∨ (z ∧ y ∧ ¬w) ∨ (x ∧ ¬z). DNF-SAT is the analog problem of (CNF-)SAT: given a DNF formula *f*, determine if there is a satisfying assignment of the corresponding variables that renders the formula true.
 - (a) Design and analyze an efficient algorithm for DNF-SAT. *Hint: DNF-SAT can be solved directly, a reduction is not needed.*

Solution: A DNF formula f is an \lor of a number of clauses. How to determine if f can be evaluated to true? If one of the clauses can be evaluated to true, then f can be evaluated to true (f is satisfiable).

For DNF, a clause is an \wedge of some terms. How to determine if a clause can be evaluated to true? A clause cannot be evaluated to true if it contains both a term x and its complement $\neg x$.

The algorithm goes as follows:

- Initilize list of variable with blank spaces that will denote their truth assignment.
- for each clause in the DNF formula:
 - mark the variable according to the literals in the clause.
 - if a variable is already marked in a way that does not correspond to the literal, then return false
- return true

This algorithm takes O(|f|) time to run because it checks the membership of \neg term for every term in *f*.

(b) Demonstrate a reduction from 3SAT to DNF-SAT and analyze its runtime. (Hint: use the distributive law.) (Another hint: this reduction will not be efficient, it will not be a polynomial-time reduction).

Solution: Both 3SAT and DNF-SAT want to check satisfiability. If we can convert an arbitrary 3CNF formula f into an equivalent DNF formula, then we can use the algorithm for DNF-SAT to solve 3SAT.

An 3CNF formula *f* is an \land of a number of clauses where each clause is an \lor of three terms. Consider an example with two clauses: $(x \lor y \lor z) \land (\neg x \lor y \lor \neg z)$. Using the distributive law, we can convert the 3CNF into an equivalent DNF with nine clauses where each clause has two terms in it: $(x \land \neg x) \lor (x \land y) \lor (x \land \neg z) \lor (y \land \neg x) \lor (y \land y) \lor (y \land \neg z) \lor (z \land \neg x) \lor (z \land \neg z)$.

In general, for a 3CNF formula f with n clauses, we can use the distributive law to find an equivalent DNF with 3^n clauses where each clause has n terms in it. Therefore, the reduction from 3SAT to DNF-SAT takes $O(n3^n)$ time to run.

4. A **Hamiltonian cycle** in a graph is a cycle that visits every vertex exactly once. A **Hamiltonian path** in a graph is a path that visits every vertex exactly once, but it need not be a cycle (the last vertex in the path may not be adjacent to the first vertex in the path.)

Consider the following three problems:

- *Directed Hamiltonian Cycle* problem: checks whether a Hamiltonian cycle exists in a *directed* graph,
- Undirected Hamiltonian Cycle problem: checks whether a Hamiltonian cycle exists in an undirected graph.
- *Undirected Hamiltonian Path* problem: checks whether a Hamiltonian path exists in an *undirected* graph.
- (a) Give a polynomial time reduction from the *directed* Hamiltonian cycle problem to the *undirected* Hamiltonian cycle problem.

Solution: For any arbitrary directed graph $G_d := \{V_d, E_d\}$, construct the following undirected graph $G_u := \{V_u, E_u\}$:

- $V_u := \{v_{in}, v_{mid}, v_{out} | v \in V_d\}$. For each of the vertices in the directed graph, we split them into a triplet of *in*, *mid*, and *out*.
- *E_u* := {(*u_{out}*, *v_{in}*)|(*u*, *v*) ∈ *E_d*} ∪ {(*v_{in}*, *v_{mid}*), (*v_{mid}*, *v_{out}*)|*v* ∈ *V_d*}. For each of the triplets that comes from the same vertex, we connect them in the order of *in-mid-out*. The directed edges in the *V_d* become the undirected ones that connect *out* and *in* between corresponding triplets.

Notice that $|V_u| = 3 |V_d|$ and $|E_u| = |E_d| + 2 |V_d|$, so this reduction is linear.

⇒: Suppose that in G_d there exists a Hamiltonian cycle $C_d := (c_1, c_2, ..., c_{|V_d|})$, where $c_i \in V_d$. Then in G_u there should also exist

 $C_{u} := (c_{1in}, c_{1mid}, c_{1out}, c_{2in}, c_{2mid}, c_{2out}, \dots, c_{|V_d|_{in}}, c_{|V_d|_{mid}}, c_{|V_d|_{out}}),$

which is a Hamiltonian cycle in G_u .

 \Leftarrow : Suppose that in G_u there exists a Hamiltonian cycle C'_u . By definition, within each of the triplets there should only be a path of order *in-mid-out*, and between two triplets there should only be an edge of *out-in*. Thus, C'_u should always be of the following form

$$C'_{u} := \left(c'_{1in}, c'_{1mid}, c'_{1out}, c'_{2in}, c'_{2mid}, c'_{2out}, \dots, c'_{|V_{d}|_{in}}, c'_{|V_{d}|_{mid}}, c'_{|V_{d}|_{out}}\right)$$

which corresponds to a Hamiltonian cycle $C'_d := (c'_1, c'_2, \dots, c'_{|V_d|})$ in G_d .

(b) Give a polynomial time reduction from the *undirected* Hamiltonian Cycle to *directed* Hamiltonian cycle.

Solution: This reduction is simpler than the previous one. Given an instance *G* of undirected Hamiltonian cycle, Let *G*' be the directed graph with the same vertices as *G* and containing edges $u \rightarrow v$ and $v \rightarrow u$ for every edge $uv \in G$.

(⇒) If *C* is a cycle in *G*, then *C* is also a cycle in the directed graph *G'*. For every $u \rightarrow v \in G'$, the edge uv is in *G*.

(\Leftarrow) If *C* is a cycle in *G'*, then *C* is also a cycle in the original graph *G*. For every $uv \in G$, the edge $u \rightarrow v \in G'$ by the construction.

(c) Give a polynomial-time reduction from undirected Hamiltonian *Path* to undirected Hamiltonian *Cycle*.

Solution: Let the input to this problem be an undirected graph *G*. The goal is to produce G' such that *G* has a Hamiltonian path if G' has a Hamiltonian cycle.

This can be done by adding a vertex v with edges to every vertex in the original graph *G*, this will be *G*'.

⇒: If there exists a Hamiltonian path *P* in *G*, starting with vertex *s* and ending with vertex *t*. Then [v, s, P, t, v] is a Hamiltonian cycle in *G*'.

 \Leftarrow : In the other case, if *C* is the Hamiltonian cycle in *G'*, then removing *v* from *C* will return a Hamiltonian path in *G*.